

## AMENDMENTS TO THE CLAIMS

1. (Currently amended) An integrated circuit device package, comprising:

an integrated circuit device having an electrically active surface and an opposing backside surface and sides extending therebetween, said electrically active surface having a plurality of electrically active circuit traces formed thereon and metallized bumps extending from selected sites on said circuit traces, and said sides including at least one feature that is effective to limit the ingress of moisture along an interface between said integrated circuit device and a dielectric molding resin, mechanically lock said integrated circuit device to said dielectric molding resin and cause said backside surface area to be less than said electrically active surface area;

a plurality of electrically conductive leads each having respective first surfaces and opposing second surfaces;

a plurality of electrical contacts extending outward from said respective first surfaces; and

a solder electrically and mechanically bonding said metallized bumps to said second surfaces;

wherein

said dielectric molding resin is formed into a package at least partially encapsulating said integrated circuit device and said plurality of electrically conductive leads,

said backside surface and said plurality of electrical contacts are exposed on opposing sides of said package, and

said interface has an end portion between said integrated circuit device at the exposed backside surface and the dielectric molding resin adjacent thereto, and an interior portion substantially parallel to the backside surface, and

said electrically active surface includes a central region and a peripheral region, said metallized bumps disposed in the peripheral region and said dielectric molding resin covering the central region.

2. (Canceled)
3. (Previously presented) The package of claim 1 wherein said at least one feature includes two elements that intersect at an angle of approximately 90°.
4. (Previously presented) The package of claim 1 wherein a thickness of said package is less than three times a thickness of said integrated circuit device.
5. (Previously presented) The package of claim 4 wherein said thickness of said package is approximately 0.01 inch.
6. (Previously presented) The package of claim 1 wherein said integrated circuit device is a sensor responsive to external stimulus.
7. (Previously presented) The package of claim 6 wherein said external stimulus is a touch.
8. – 18. (Canceled)